|  |  |
| --- | --- |
| essg | Escola Secundária de Sebastião da Gama |
| Sistemas Digitais e Arquitetura de Computadores – Módulo 3 |
| Ano 10º - Turma N |
| Prof. Carlos Pereira |

Um **multiplexador**, multiplexer, mux ou multiplex é um dispositivo que seleciona as informações de duas ou mais fontes de dados num único canal.

*Relatório da Experiência 4*

***Circuitos combinatórios***

**multiplexador 2/1.**

[**www.TicMania.net**](http://www.TicMania.net)

## Situação/problema

Este relatório terá como objetivo desenvolver um multiplexador 2/1.

## Tabela de verdade e Expressão booleana

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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| |  |  |  |  | | --- | --- | --- | --- | | **Seletor** | **Entradas** | | **Saída** | | S | B | A | Y | | 0 | 0 | 0 | 0 | | 0 | 0 | 1 | 1 | | 0 | 1 | 0 | 0 | | 0 | 1 | 1 | 1 | | 1 | 0 | 0 | 0 | | 1 | 0 | 1 | 0 | | 1 | 1 | 0 | 1 | | 1 | 1 | 1 | 1 | | |  |  | | --- | --- | | **Seletor** | **Saída** | | S | Y | | 0 | Z0 | | 1 | z1 | |

Y=SB+S’A

## Circuito lógico

